



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re the Application of	)	
Richard Uhlig, et al.	)	Examiner: K. Takeguchi
Serial No.: 10/676,737	)	Art Unit: 2187
Filing Date: September 30, 2003	)	
For: Method For Resolving Address Space	)	
Conflicts Between A Virtual Machine	)	
Monitor And A Guest Operating	)	
System	)	

Commissioner for Patents  
U.S. Patent and Trademark Office  
Washington, D.C. 20231

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Respectfully submitted,

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/676,737
Filing Date	September 30, 2003
First Named Inventor	Uhlig, Richard
Group Art Unit	2187
Examiner Name	Takeguchi, Kathy

Sheet 1 of 1

Attorney Docket No: 42P09768C

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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		SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", <u>Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA, (Nov. 2001)</u> ,	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

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